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Reply under 37 CFR 1.116
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 10/671,229
Confirmation No. 8204
Filing Date September 24, 2003
Inventor Leonard Forbes
Assignee Micron Technology, Inc.
Group Art Unit 2814
Examiner L. Pham
Attorney's Docket No. MI22-2272
Customer No. 021567
Title: Electronic Apparatus, Silicon-on-Insulator Integrated Circuits, and
Fabrication Methods

RESPONSE TO SEPTEMBER 25, 2006 FINAL OFFICE ACTION

To: MS AF
Commissioner for Patents and Trademarks
P.O. Box 1450
Art Unit 2814
Alexandria, VA 22313-1450

From: James E. Lake (Tel. 509-624-4276; Fax 509-838-3424)
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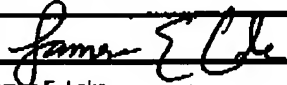
AMENDMENTS

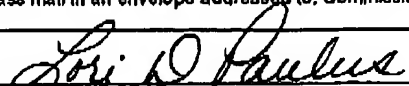
PTO/SB/21 (09-06)

Approved for use through 03/31/2007. OMB 0851-0031
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TRANSMITTAL FORM (To be used for all correspondence after initial filing)	Application Number	10/671,229
	Filing Date	September 24, 2003
	First Named Inventor	Leonard Forbes
	Art Unit	2814
	Examiner Name	L. Pham
	Attorney Docket Number	M122-2272
Total Number of Pages in This Submission		0

ENCLOSURES (Check all that apply)		
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Remarks Customer No. 021567 Although there is no fee believed due, the Commissioner is hereby authorized to charge any fees under 37 C.F.R. 1.16 and 1.17 which may be required by this paper to Deposit Account No. 23-0925.		
SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT		
Firm Name	Wells St. John P.S.	
Signature		
Printed name	James E. Lake	
Date	22 Dec 2006	Reg. No. 44,654

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Typed or printed name	Lori D. Paulus	Date 12/22/06

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